

# TCS3-150-2 校正基板説明書

株式会社テクノプローブ

## 1. パターンリスト

配列: GSGSG

パッドサイズ: 50  $\mu\text{m}$  x 50  $\mu\text{m}$ パッドピッチ: 150  $\mu\text{m}$ 

パターン名称	搭載素子記号	用途	素子の位置
Open	O (No.1 ~ No.7)	SOLT, TRL	No.1 : (3.0, 18.0) : (13.8, 18.0) Y: 2.0 mm pitch
Short	S (No.1 ~ No.7)	SOLT, TRL	No.1 : (4.35, 18.0) : (15.15, 18.0) Y: 2.0 mm pitch
Load	L (No.1 ~ No.7)	SOLT	No.1 : (5.7, 18.0) : (16.5, 18.0) Y: 2.0 mm pitch
Line TH	TH (No.1 ~ No.7)	THRU for SOLT (1.86 ps delay)	No.1 : (7.05, 18.0) : (17.85, 18.0) Y: 2.0 mm pitch
Loop-Back Thru1	LB-TH1 (No.1 ~ No.7)	Loop Back Thru (3.39 ps delay)	No.1 : (8.4, 18.0) : (19.2, 18.0) Y: 2.0 mm pitch
Loop-Back Thru2	LB-TH2 (No.1 ~ No.7)	Loop Back Thru (3.39 ps delay)	No.1 : (9.75, 18.0) : (20.55, 18.0) Y: 2.0 mm pitch
Cross Thru1	C-TH1	Cross Thru1 (2.91 ps delay)	No.1 : (11.1, 18.0) : (21.9, 18.0) Y: 2.0 mm pitch
Cross Thru2	C-TH2	Cross Thru2 (2.91 ps delay)	No.1 : (12.45, 18.0) : (23.25, 18.0) Y: 2.0 mm pitch
Line 100	L1	LINE for TRL (100 GHz) (4.36 ps delay)	(3.0, 3.0)
Line 80	L2	LINE for TRL (80 GHz) (4.99 ps delay)	(6.0, 3.0)
Line 60	L3	LINE for TRL (60 GHz) (6.03 ps delay)	(9.0, 3.0)

Line 40	L4	LINE for TRL (40 GHz) (8.11 ps delay)	(12.0, 3.0)
Line 20	L5	LINE for TRL (20 GHz) (14.36 ps delay)	(16.5, 3.0)
Line 10	L6	LINE for TRL (10 GHz) (26.86 ps delay)	(21.0, 3.0)
Line 5	L7	LINE for TRL (5 GHz) (51.86 ps delay)	(4.5,1.0)
Line 2.5	L8	LINE for TRL (2.5 GHz) (101.86 ps delay)	(16.5,1.0)
Mark	M-1, M-2, M-3, M-4	アライメントマーク	M-1: (0, 0) ,基準点 M-2: (24.0, 0) M-3: (24.0, 19.0) M-4: (0, 19.0)

## 2. Substrate top view

Thickness: 0.635 mm

### TCS3-150-2 (Pattern: GSGSG, Pitch: 150μm)

